

FIG. 1

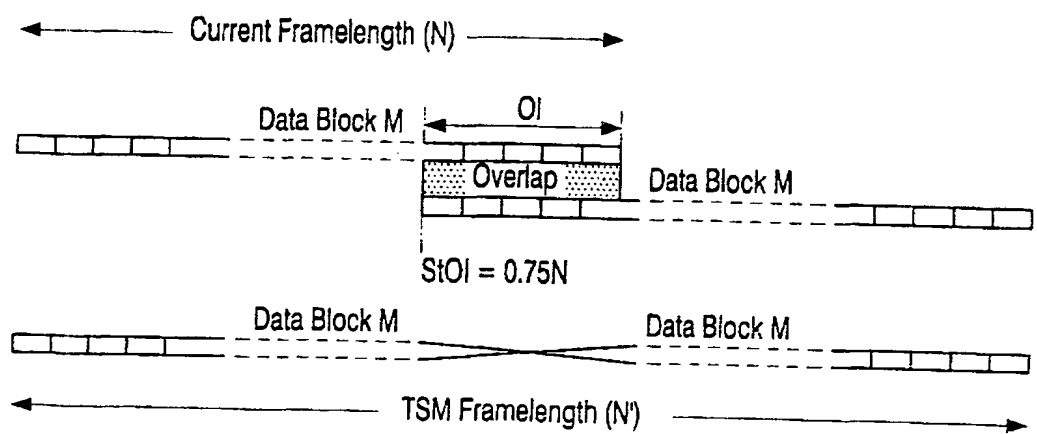


FIG. 2

Prior Art

2/5

Analysis Block

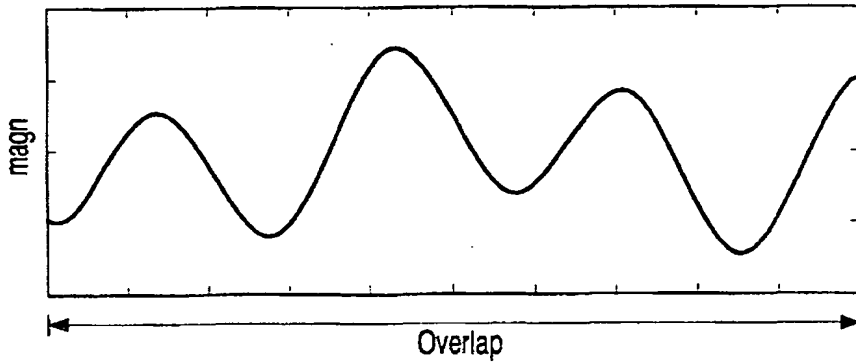


FIG. 3a (PRIOR ART)

Synthesis Block

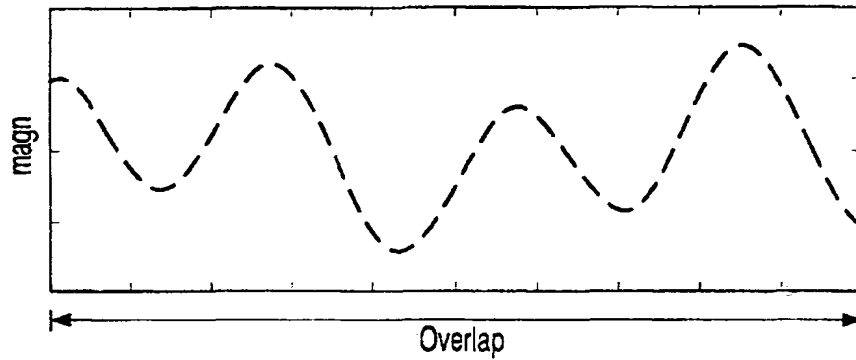


FIG. 3b (PRIOR ART)

Synchronisation

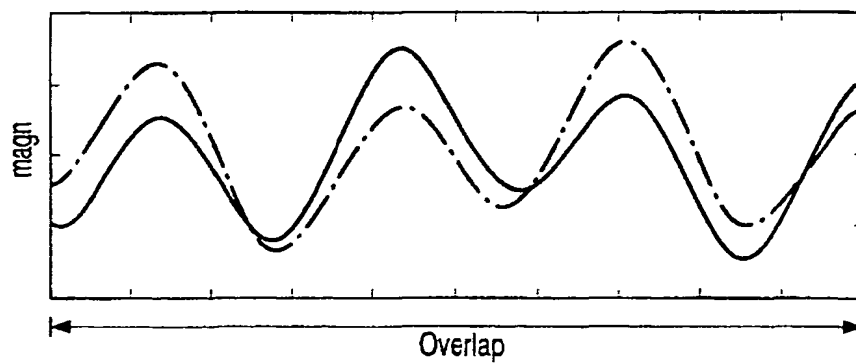


FIG. 3c (PRIOR ART)

3/5

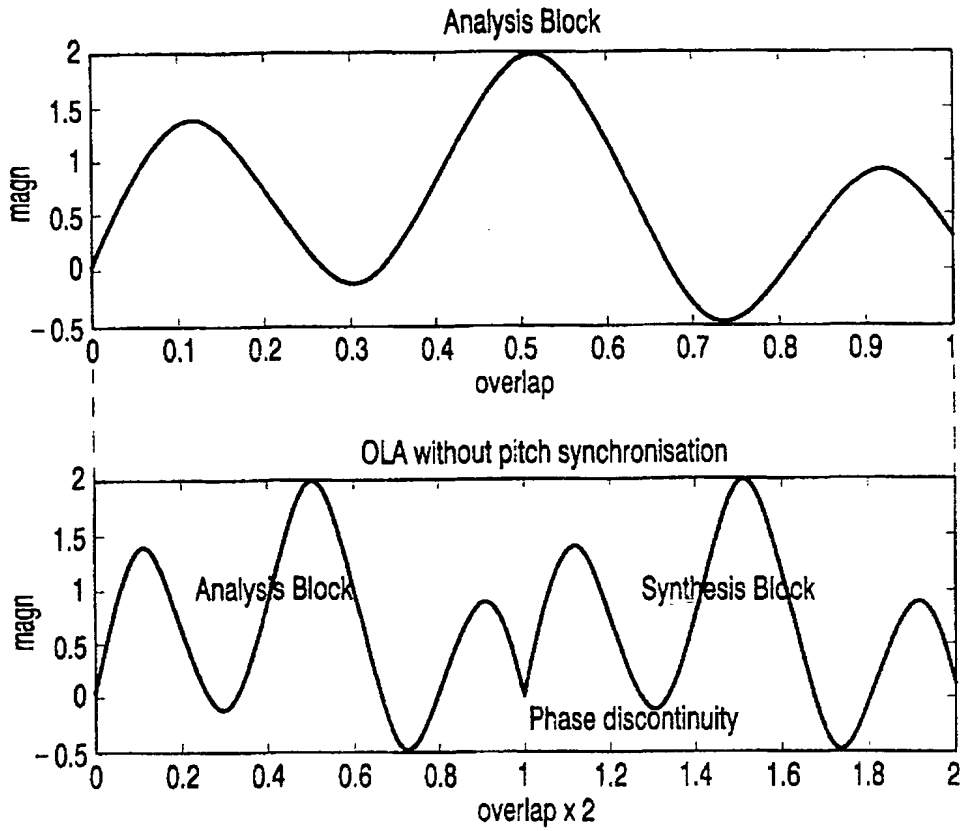


FIG. 4 (PRIOR ART)

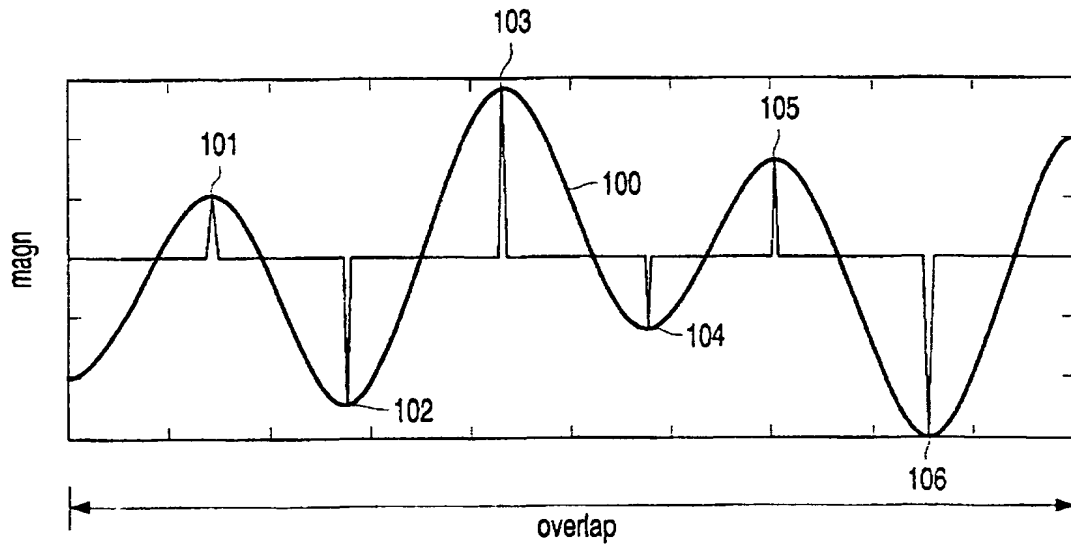


FIG. 5